

# Notice of Allowability

Application No.

09/850,053

Examiner

Prabodh M Dharja

Applicant(s)

INUKAI, KAZUTAKA

Art Unit

2673

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 11-29-2004.
2. ☒ The allowed claim(s) is/are 36-49 and 132-167 and renumbered as 1-50.
3. ☒ The drawings filed on 08 May 2001 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
  - \* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 08-27-03
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

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1. **Status:** Receipt is acknowledged of papers submitted 11-19-2004, under request for reconsideration which, have been placed of record in the file. Claims 36-49 and 132-167 are pending in this office action.

2. Claims 36-49 and 132-167 are allowed.

3. The following is an examiner's statement of reasons for allowance:

a light emitting device comprising: a source signal line driver circuit; a gate signal line driver circuit; an opposing power source line driver circuit; a pixel portion comprising a plurality of pixels; a plurality of source signal lines connected to the source signal line driver circuit; a plurality of gate signal lines connected to the gate signal line driver circuits; a plurality of opposing power source lines connected to the opposing power source line driver circuit; and a plurality of power source supply lines, wherein each pixel comprises: a switching TFT having a gate electrode connected to any one of the plural gate signal lines and a source region and a drain region, one of which is connected to any one of the plural source signal lines; an electro luminescence driver TFT, a gate electrode of said electro luminescence driver TFT connected to the other of said source region and said drain region of switching TFT; an electro luminescence element comprises a pixel electrode, an opposing electrode connected to any one of the plural opposing power source lines, and an electro luminescence layer provided between the pixel electrode and the opposing electrode; wherein the electro luminescence driver TFT has a source region connected to any one of

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**the plural power source supply lines and the electro luminescence driver TFT has a drain region connected to the pixel electrode.**

The cited references on 892's fails to anticipate individually as well as render obviousness individually or in combination above bold underlined recited claim.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### *Conclusion*

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M Dharia whose telephone number is 703-605-1231. The examiner can normally be reached on M-F 8AM to 5PM.
5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703-3054938. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.
6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

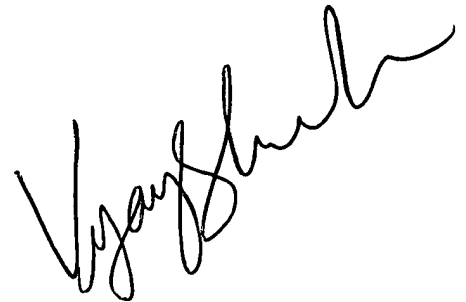
Commissioner of Patents and Trademarks

Washington, D.C. 20231

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01-19-2005

A handwritten signature in black ink, appearing to read 'Vijay Shankar', written in a cursive style.

**VIJAY SHANKAR  
PRIMARY EXAMINER**